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Applicant: Shinya Hasegawa

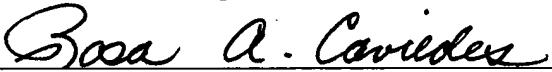
Serial No.: Not yet assigned Group Art Unit: Not yet assigned

Filed: June 28, 2001 Examiner: Not yet assigned

Title: SEMICONDUCTOR INTEGRATED CIRCUIT AND FUNCTIONAL BLOCK OF THE SAME

EXPRESS MAIL NUMBER: EL 813 645 908 USDATE OF DEPOSIT: June 28, 2001

I hereby certify that this paper is being deposited with the United States Postal Service "EXPRESS MAIL Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Box PATENT APPLICATION, Commissioner for Patents; Washington, DC 20231.



Rosa A. Caviedes

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INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
Washington, DC 20231

Dear Sir:

Pursuant to 37 CFR §1.56(a), Applicant hereby cites the following documents (copies enclosed) listed on the attached copy of Form PTO-1449.

The Commissioner is also authorized to charge any deficiencies in fees and credit any overpayment of fees to Deposit Account No. 07-1896.

Respectfully submitted,

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